

L Number	Hits	Search Text	DB	Time stamp
-	794	(375/354).CCLS.	USPAT	2004/01/07 15:29
-	207	(714/700).CCLS.	USPAT	2004/01/07 15:37
-	49736	first adj10 latch\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:38
-	45846	second adj10 latch\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:38
-	33709	(first adj10 latch\$4) and (second adj10 latch\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:38
-	4932	((first adj10 latch\$4) and (second adj10 latch\$4)) and synchroniz\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:38
-	1526	skew\$4 adj10 compensat\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:39
-	19708	data adj10 restor\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:40
-	2	((first adj10 latch\$4) and (second adj10 latch\$4)) and synchroniz\$4 and (skew\$4 adj10 compensat\$4) and (data adj10 restor\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:44
-	15	(skew\$4 adj10 compensat\$4) and (data adj10 restor\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:51
-	69624	serial adj10 data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:51
-	228306	clock adj10 signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:51
-	24711	(serial adj10 data) and (clock adj10 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:52
-	9677	predetermin\$4 adj10 offset\$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:52
-	210	((serial adj10 data) and (clock adj10 signal)) and (predetermin\$4 adj10 offset\$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:52

-	47995	latch\$4 adj10 data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:52
-	77	((serial adj10 data) and (clock adj10 signal)) and (predetermin\$4 adj10 offset\$6)) and (latch\$4 adj10 data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:52
-	126	n-bit adj10 sync\$10 adj10 signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:53
-	1	((((serial adj10 data) and (clock adj10 signal)) and (predetermin\$4 adj10 offset\$6)) and (latch\$4 adj10 data)) and (n-bit adj10 sync\$10 adj10 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:54
-	1	(predetermin\$4 adj10 offset\$6) and (latch\$4 adj10 data) and (n-bit adj10 sync\$10 adj10 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/07 15:54
-	11	((serial adj10 data) and (clock adj10 signal)) and (latch\$4 adj10 data) and (n-bit adj10 sync\$10 adj10 signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 15:25
-	6653	converg\$6 adj10 device	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 15:25
-	250	pctv or pc/tv	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 15:26
-	39	(converg\$6 adj10 device) and (pctv or pc/tv)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 15:26
-	5483	(vertical adj5 blanking adj5 interval) or vbi	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 15:26
-	9	((converg\$6 adj10 device) and (pctv or pc/tv)) and ((vertical adj5 blanking adj5 interval) or vbi)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 15:26
-	3	6407779.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 16:02
-	101	(725/133).CCLS.	USPAT	2004/01/08 17:21
-	6	5887039.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 18:09
-	1267	skew adj10 compensat\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 18:10

-	792	(skew adj10 compensat\$4) and receiv\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 18:10
-	30216	optical adj10 receiver	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 18:17
-	16	((skew adj10 compensat\$4) and receiv\$4) and (optical adj10 receiver)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 18:57
-	207	(714/700).CCLS.	USPAT	2004/01/08 19:27
-	8	5579352.uref.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:20
-	1050	non-overlap\$4 adj10 clock adj10 signal	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:21
-	2111	state adj10 latch\$4 adj10 data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:23
-	70	(non-overlap\$4 adj10 clock adj10 signal) and (state adj10 latch\$4 adj10 data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:23
-	368	width adj10 unit adj10 bit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:24
-	0	((non-overlap\$4 adj10 clock adj10 signal) and (state adj10 latch\$4 adj10 data)) and (width adj10 unit adj10 bit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:24
-	18621	width adj10 bit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:24
-	35	((non-overlap\$4 adj10 clock adj10 signal) and (state adj10 latch\$4 adj10 data)) and (width adj10 bit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:26
-	1	(non-overlap\$4 adj10 clock adj10 signal) and (width adj10 unit adj10 bit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:30
-	0	5887039.pn. and width	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:32
-	2	5887039.pn. and bit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/01/08 20:30

-	1	6477184.pn. and width	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/01/08 20:32
---	---	-----------------------	---	------------------